

FIG. 1

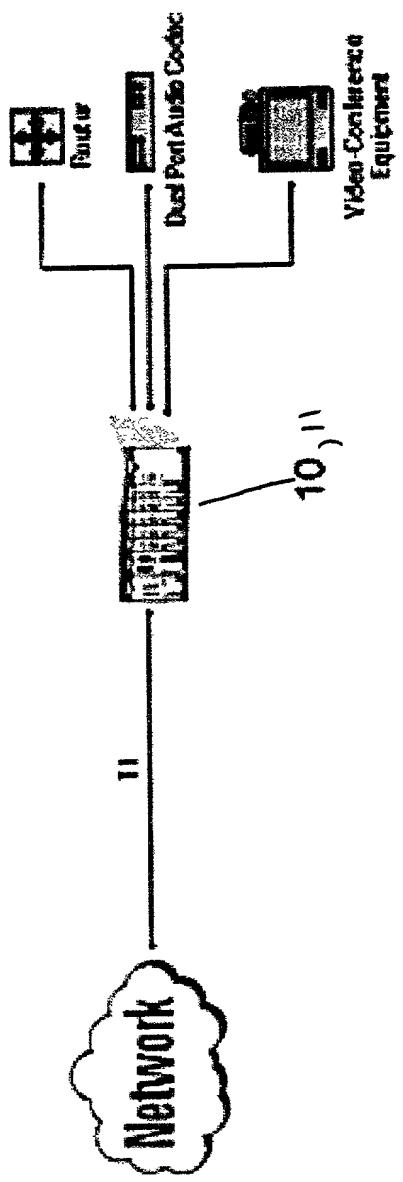


Fig. 2

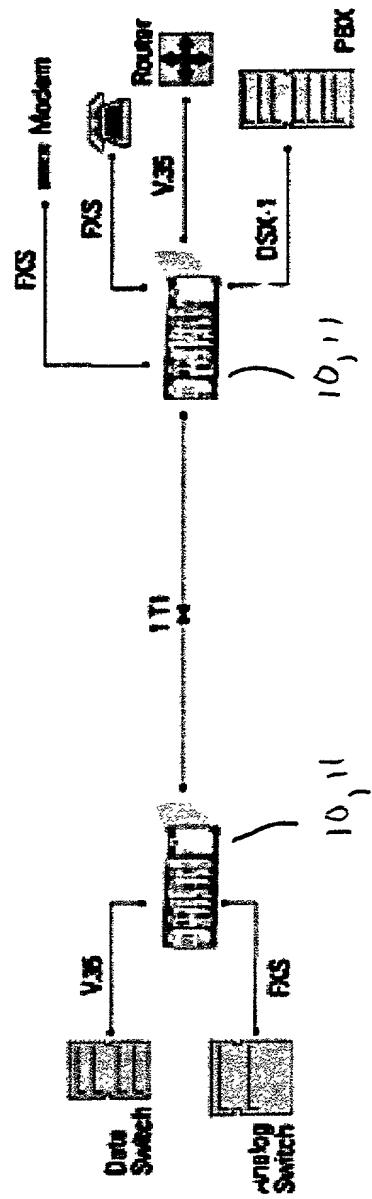


FIG. 3

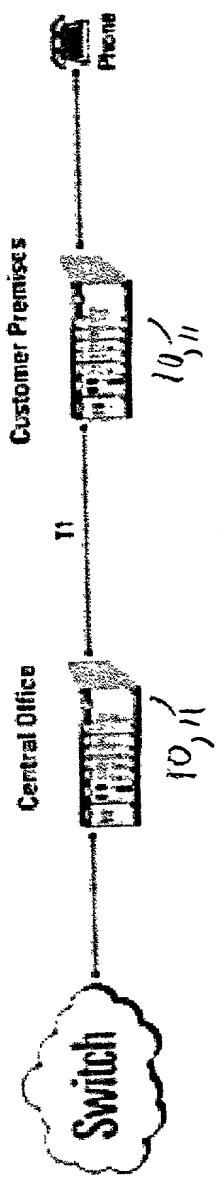


FIG. 4 (a)

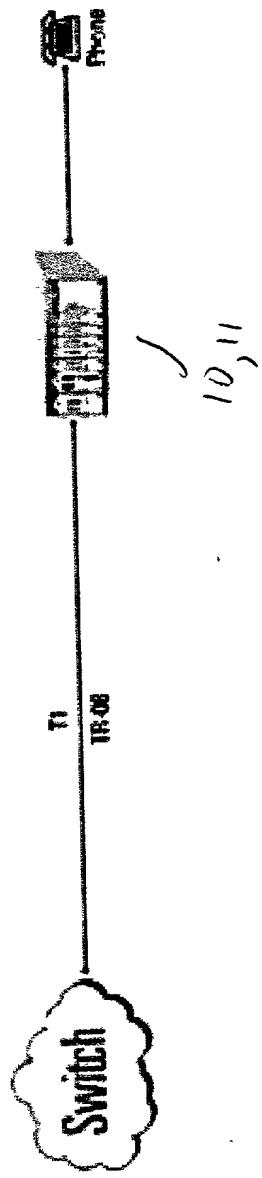
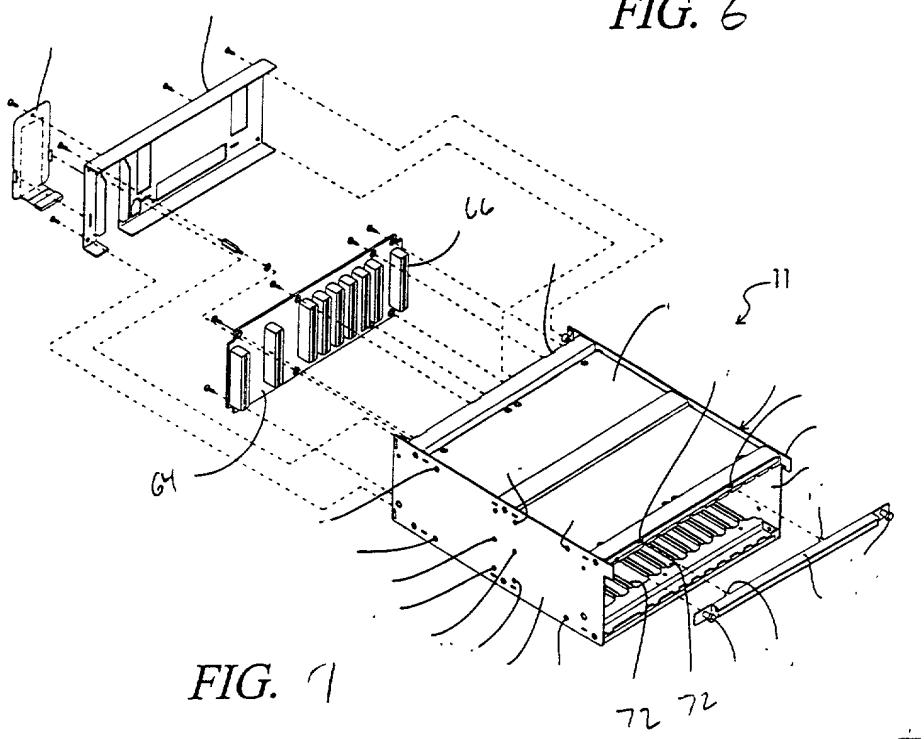
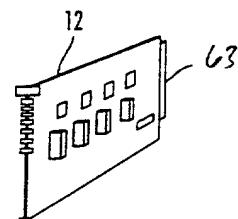
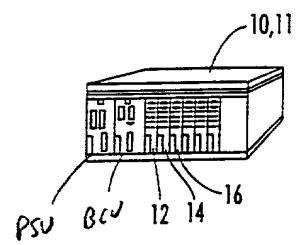


FIG. 4 (b)



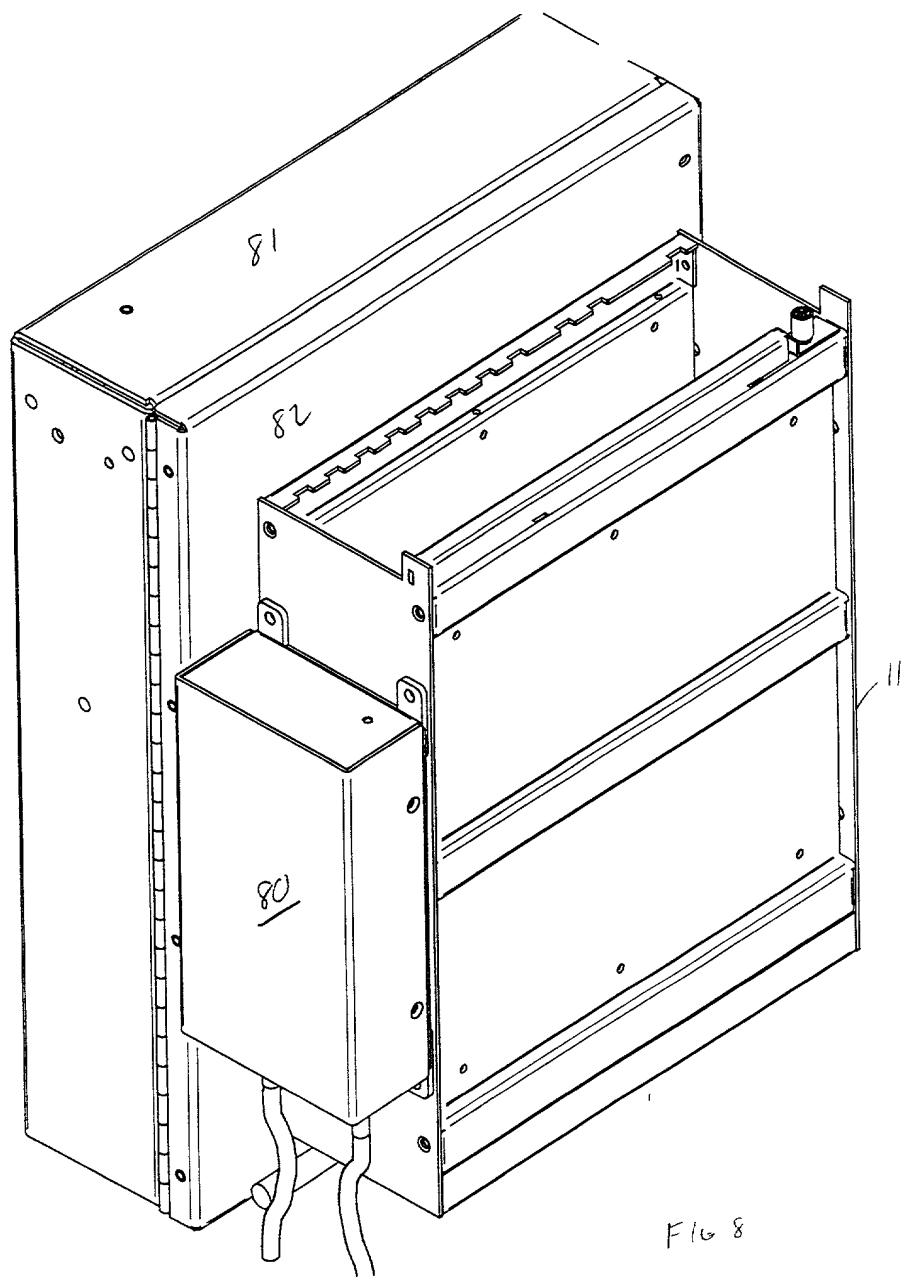


FIG. 8

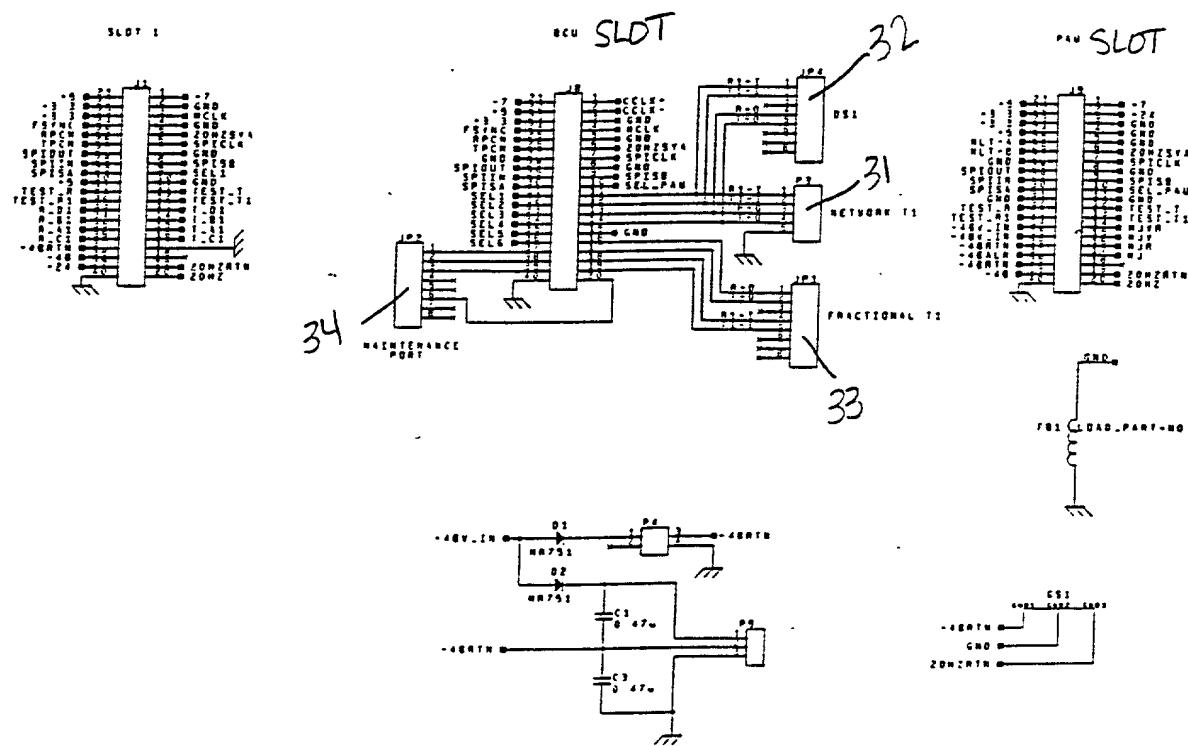
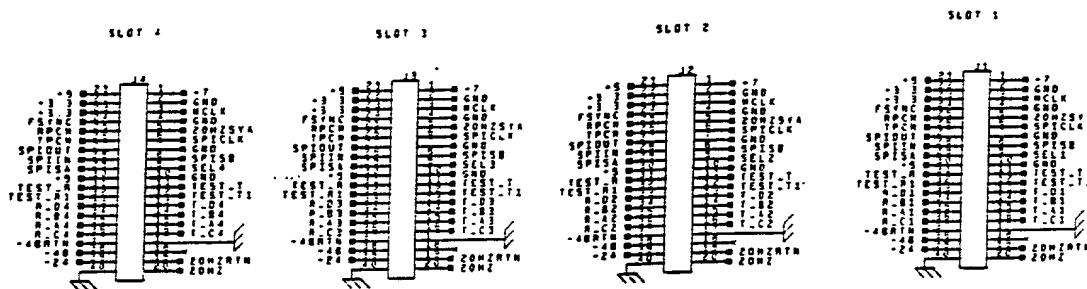


Figure 9



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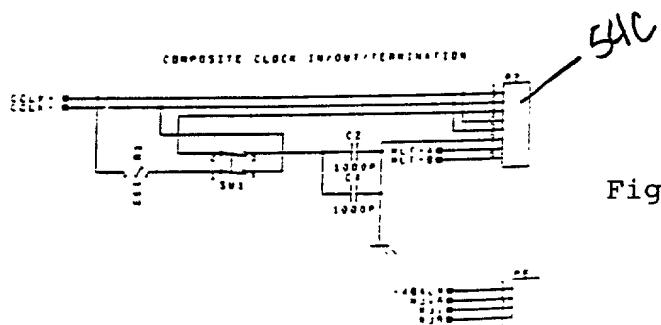
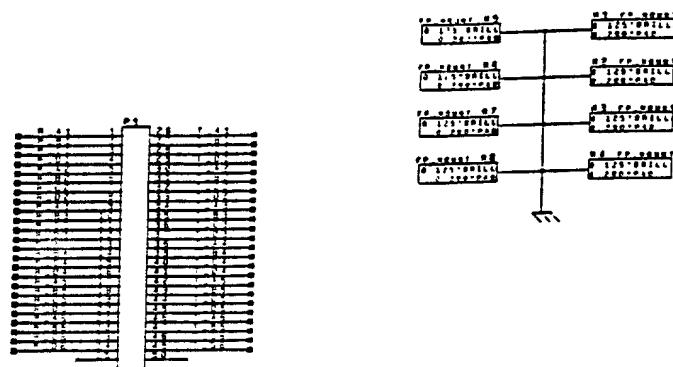


Figure 9 (Continued)

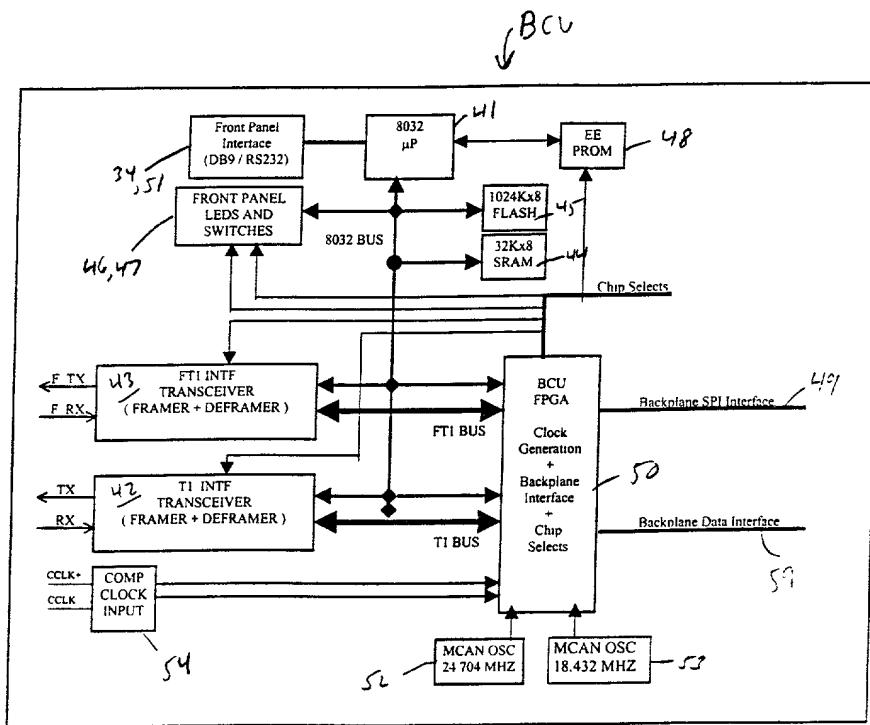


Fig. 10

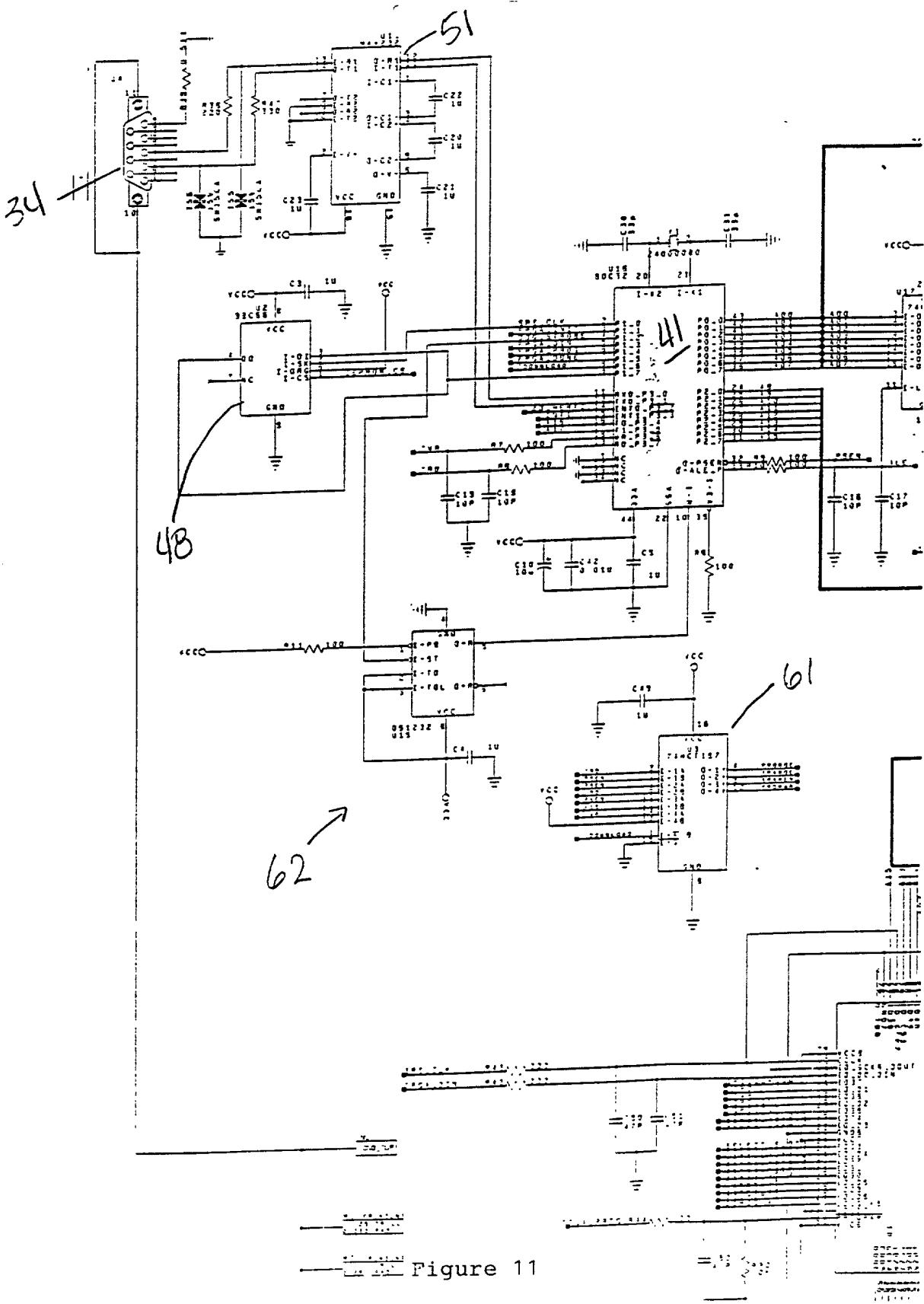
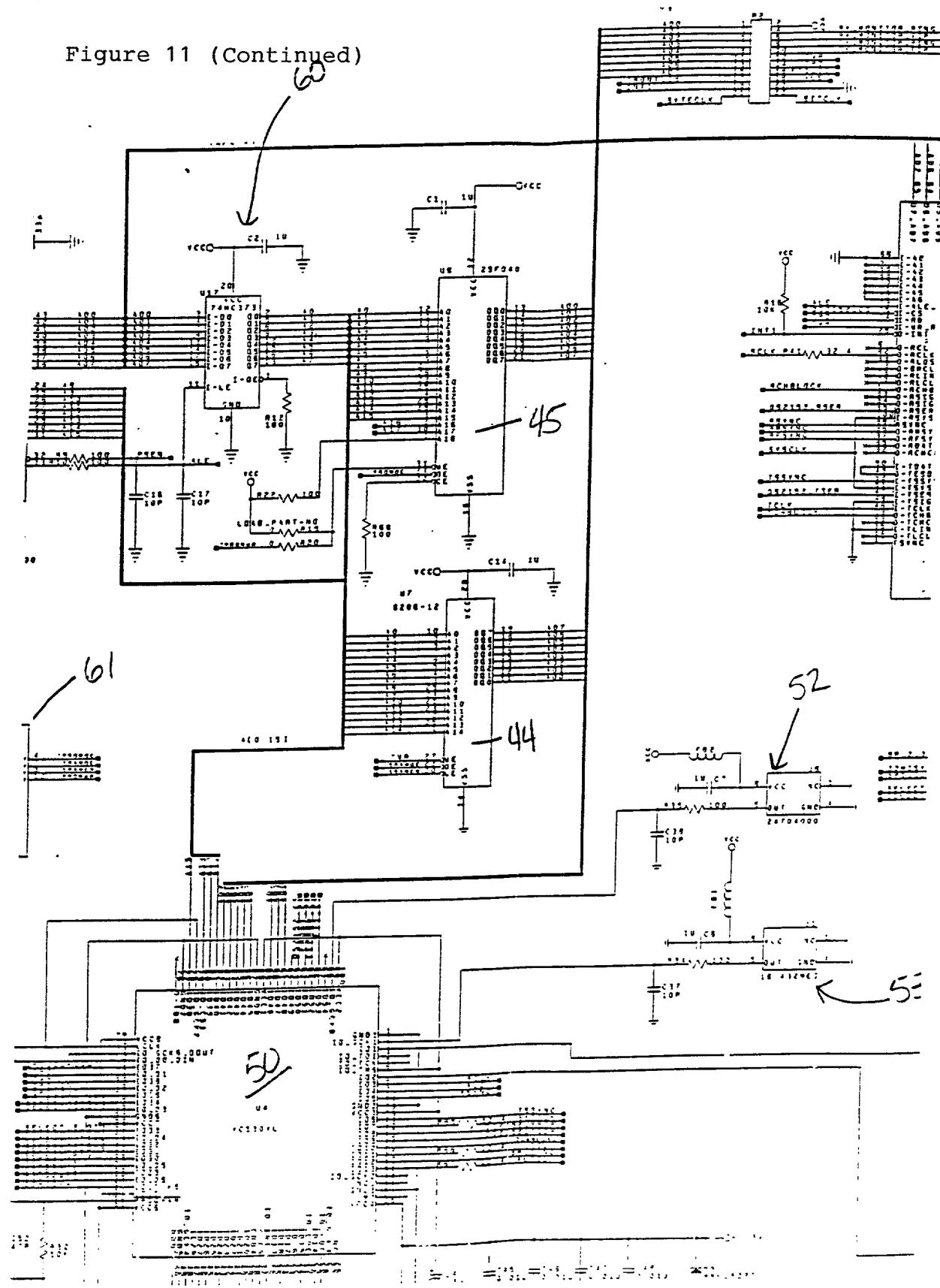


Figure 11

Figure 11 (Continued)



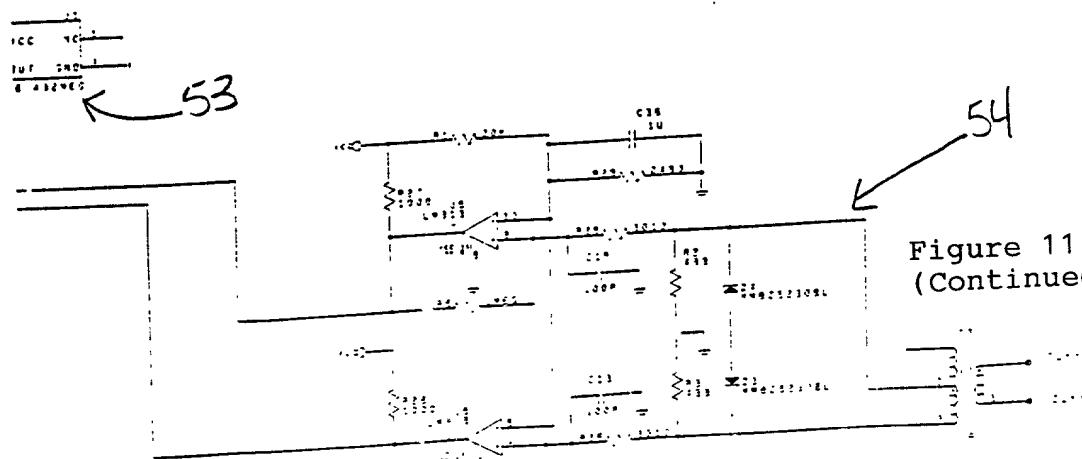
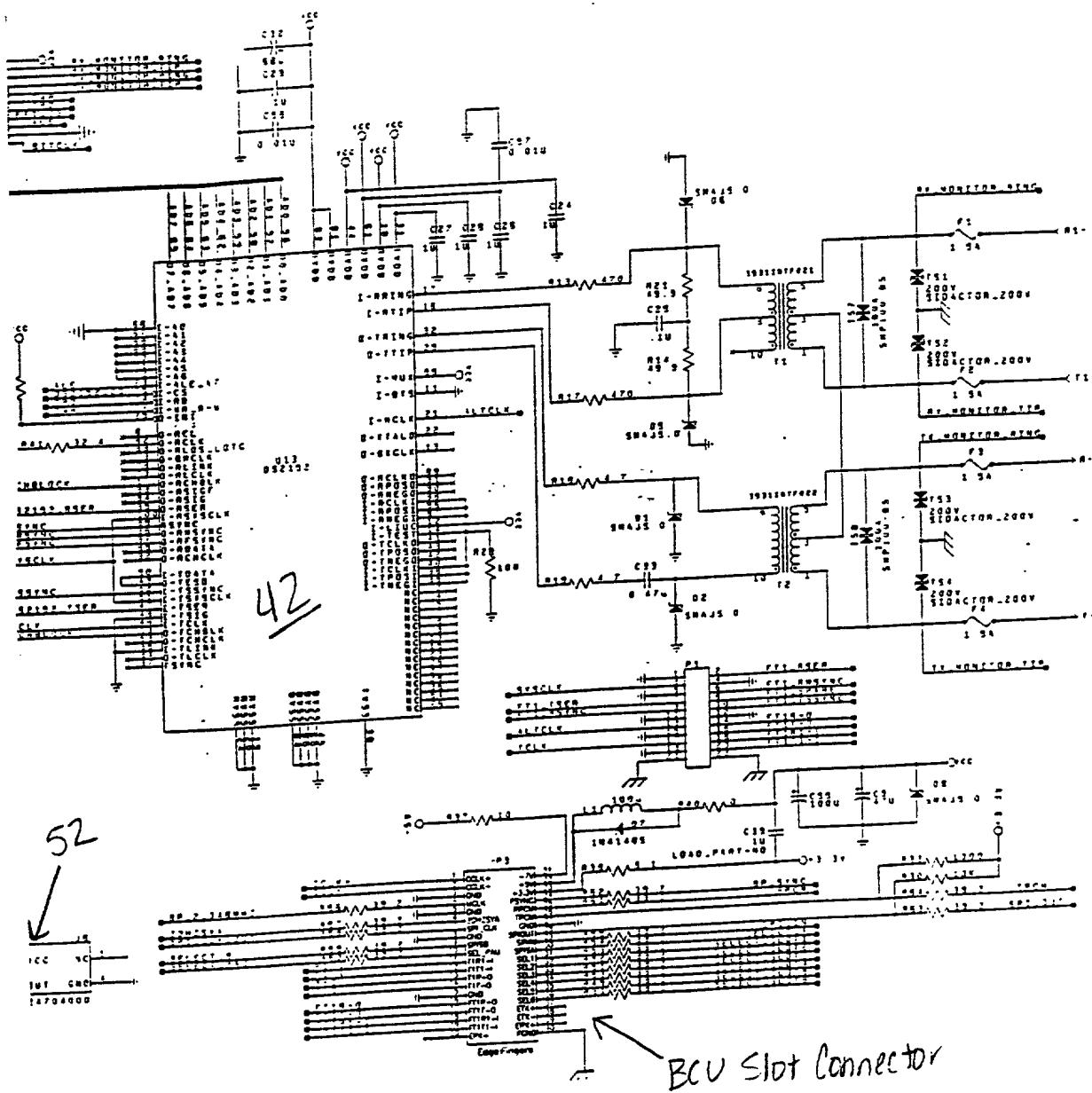


Figure 11  
(Continued)

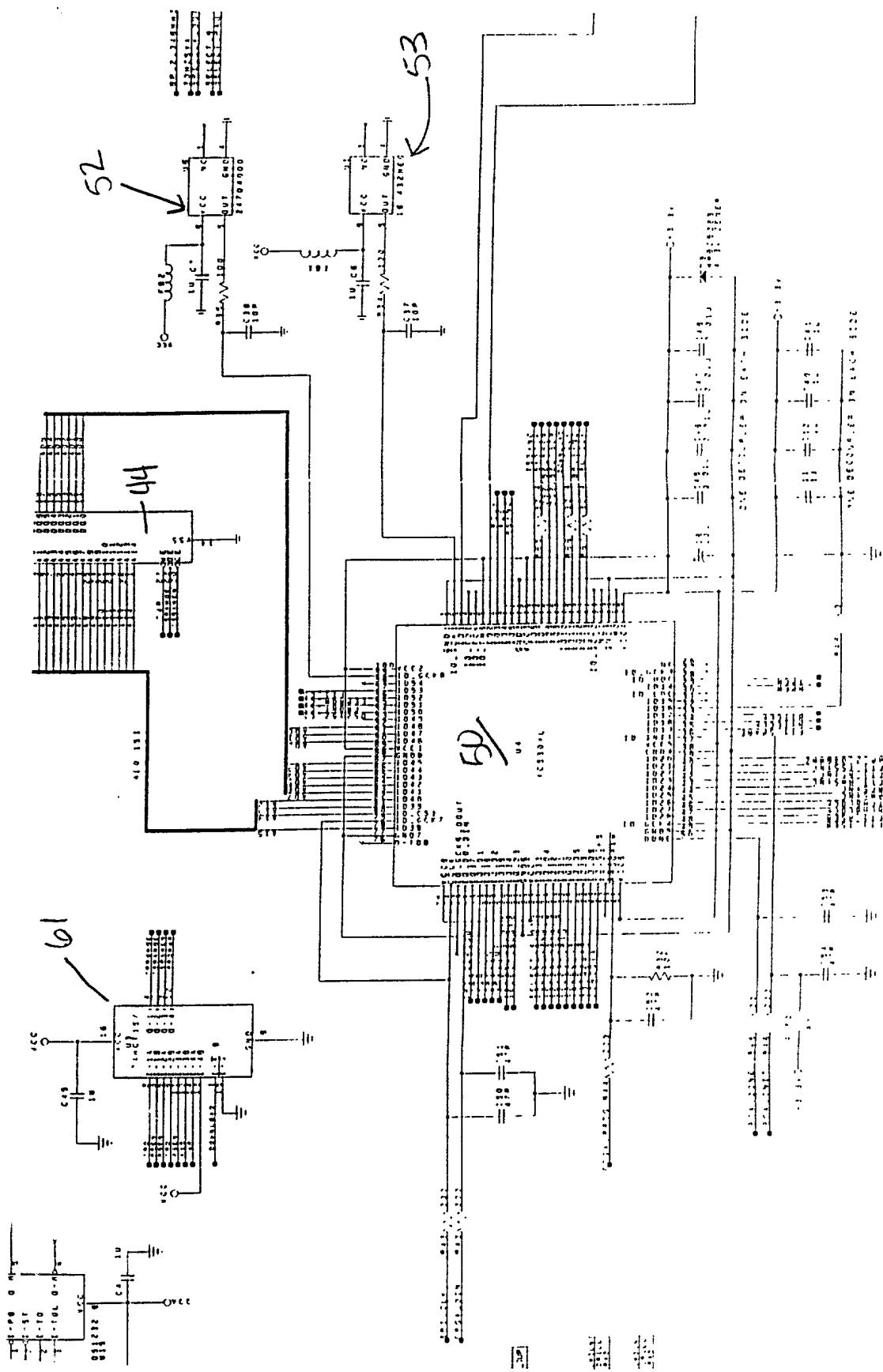
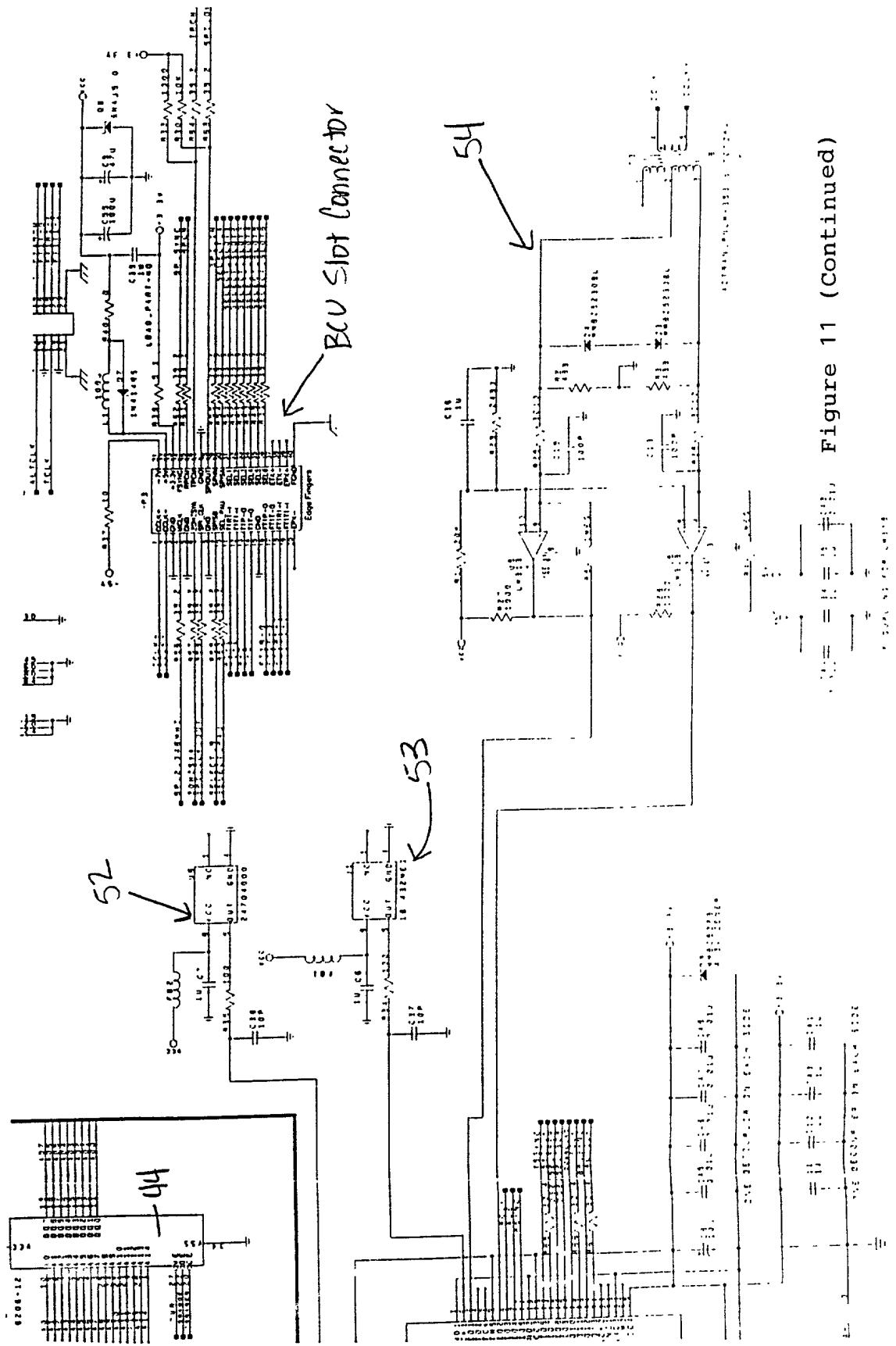


Figure 11 (Continued)



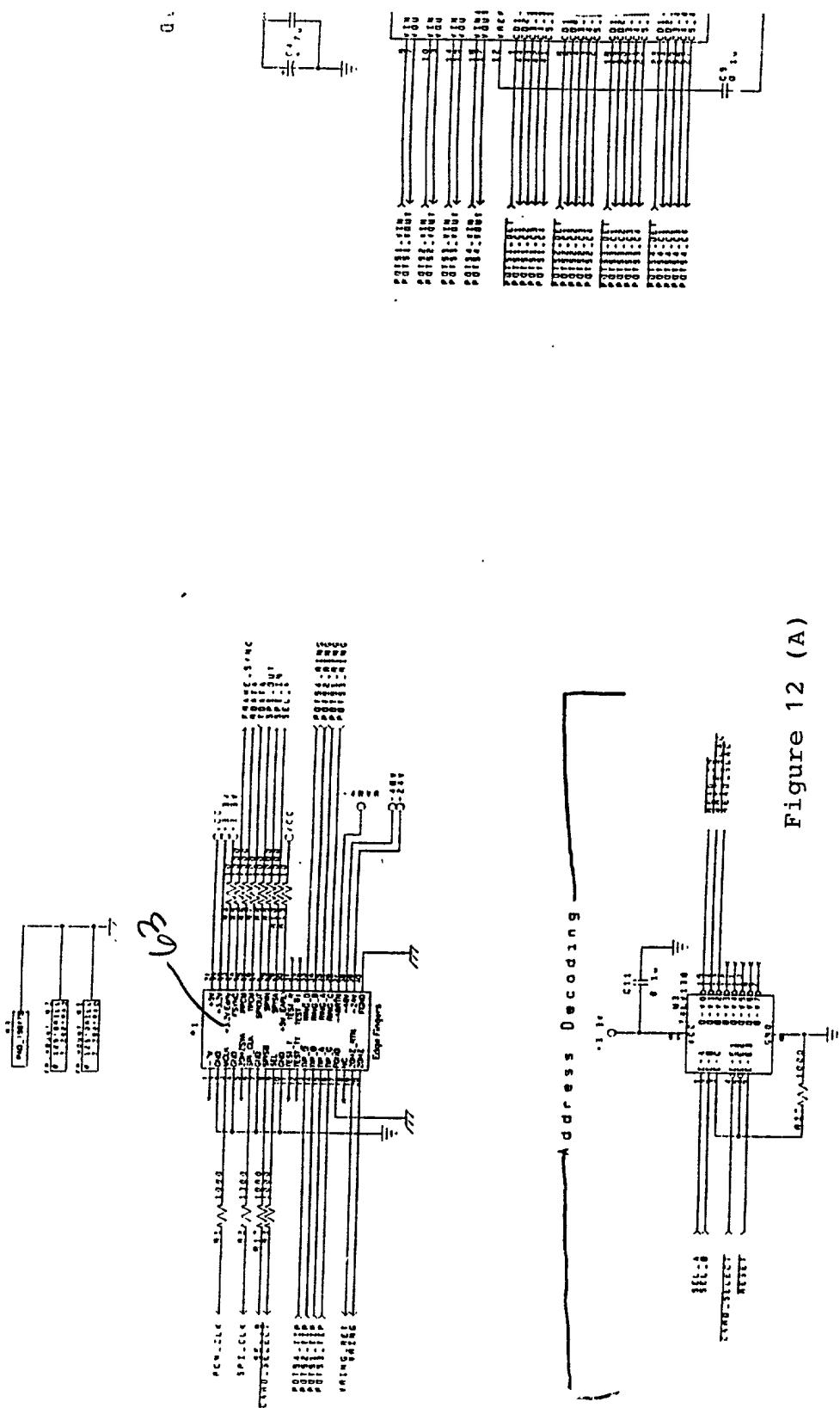


Figure 12 (A)

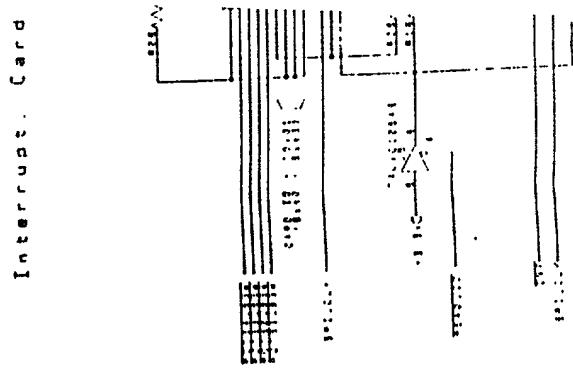
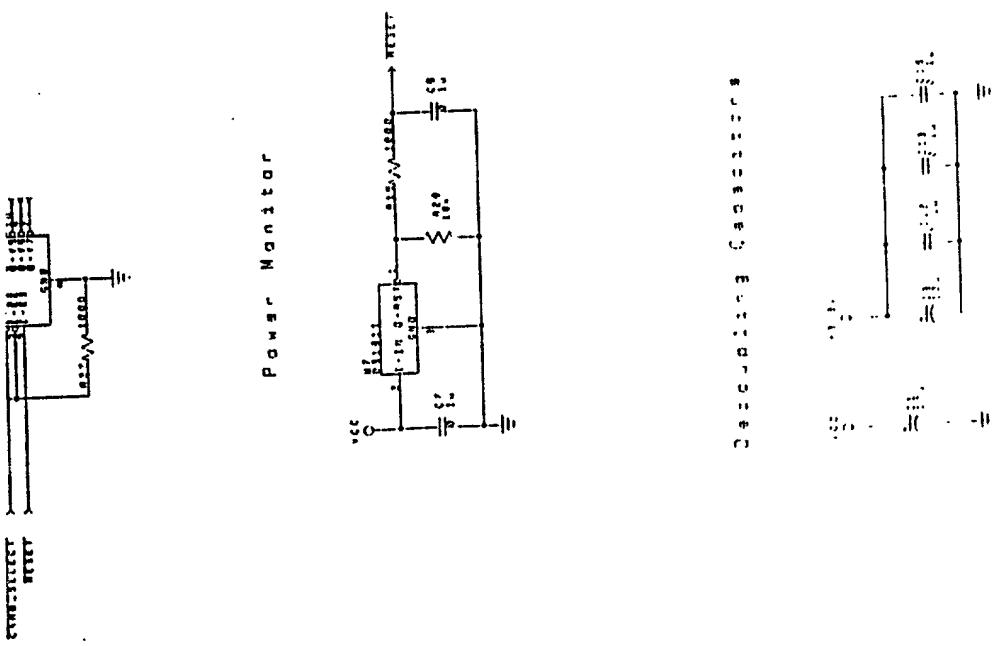


Figure 12(A) (Continued)

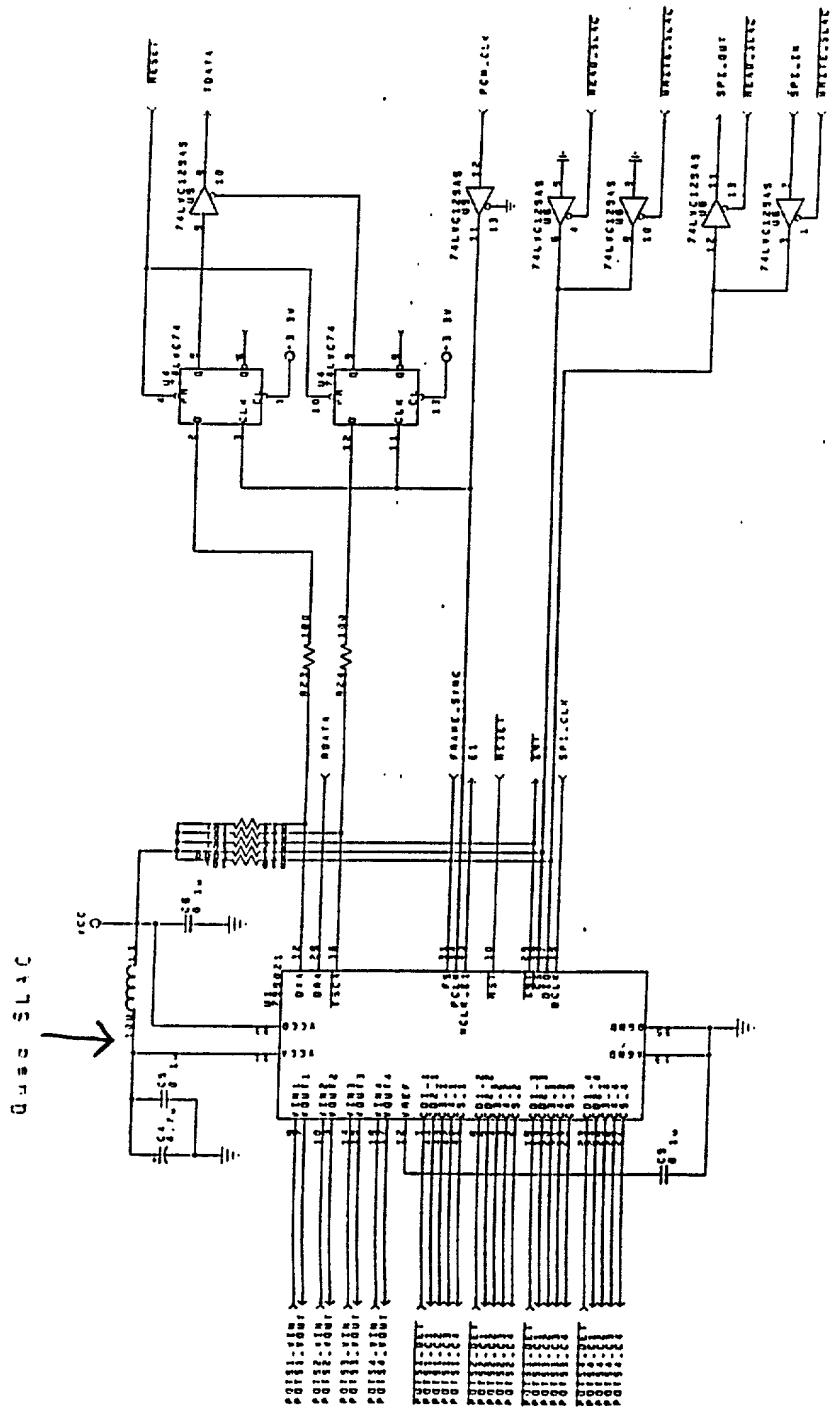


Figure 12(A) (Continued)

Interrupt Card IO and Battery Switch Register

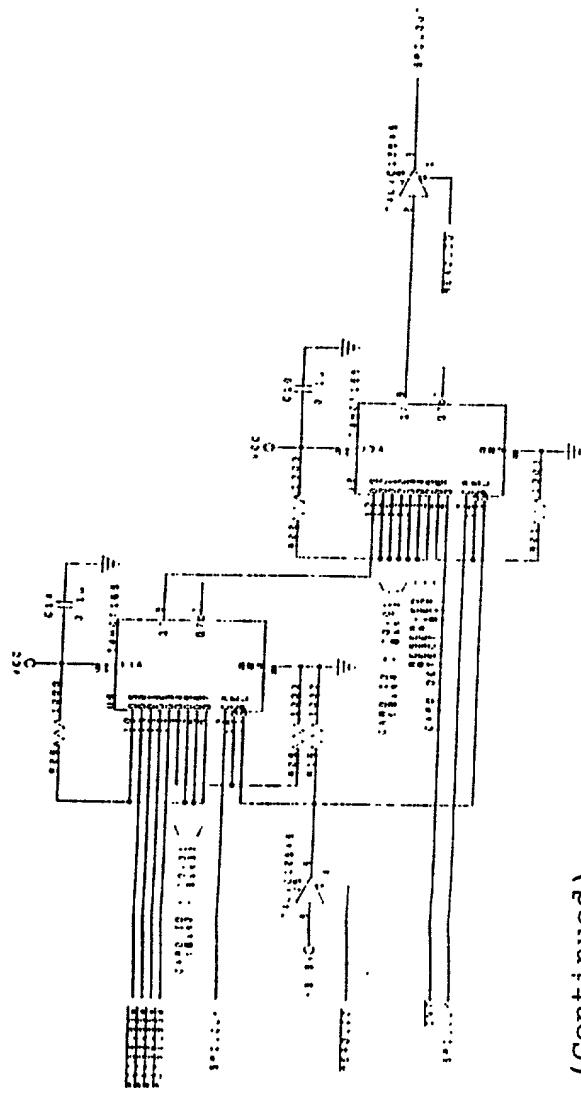
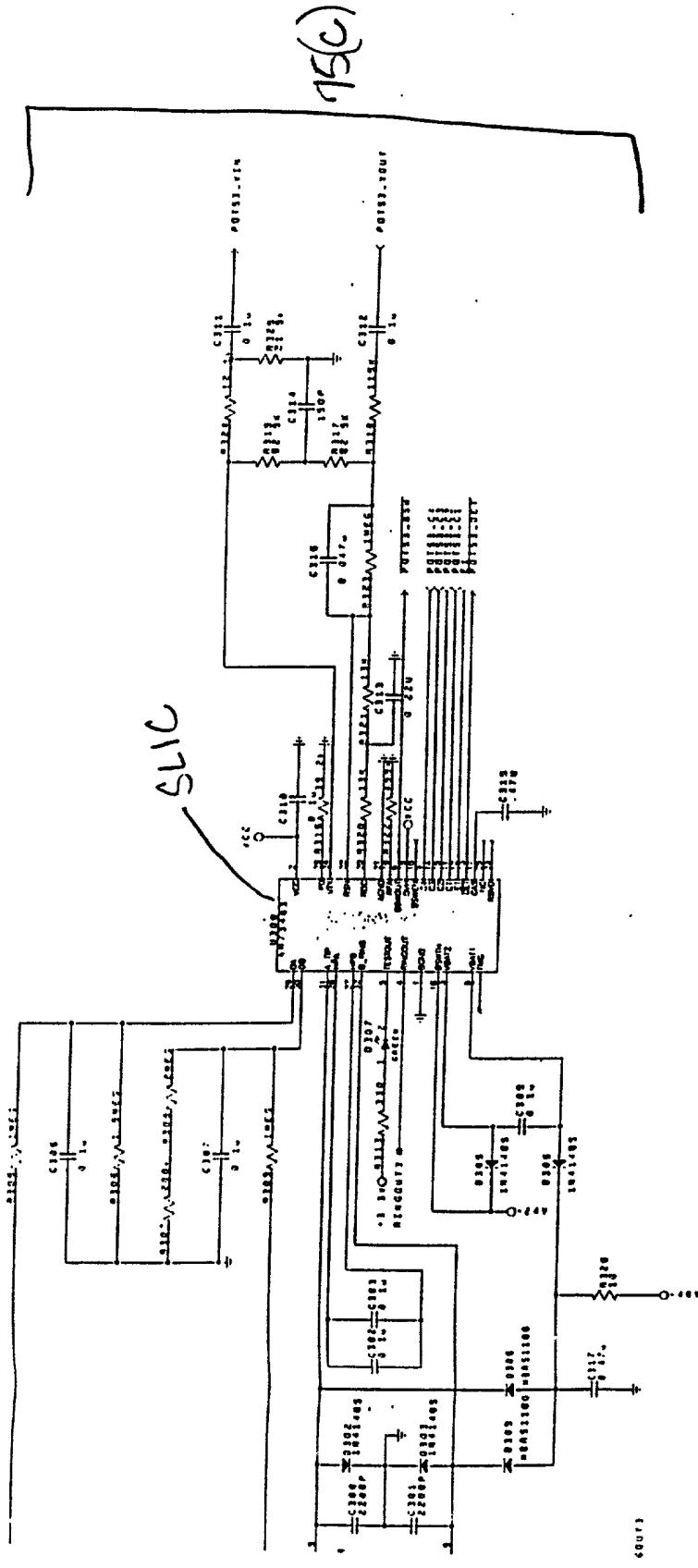


Figure 12(A) (Continued)

Figure 12 (B)



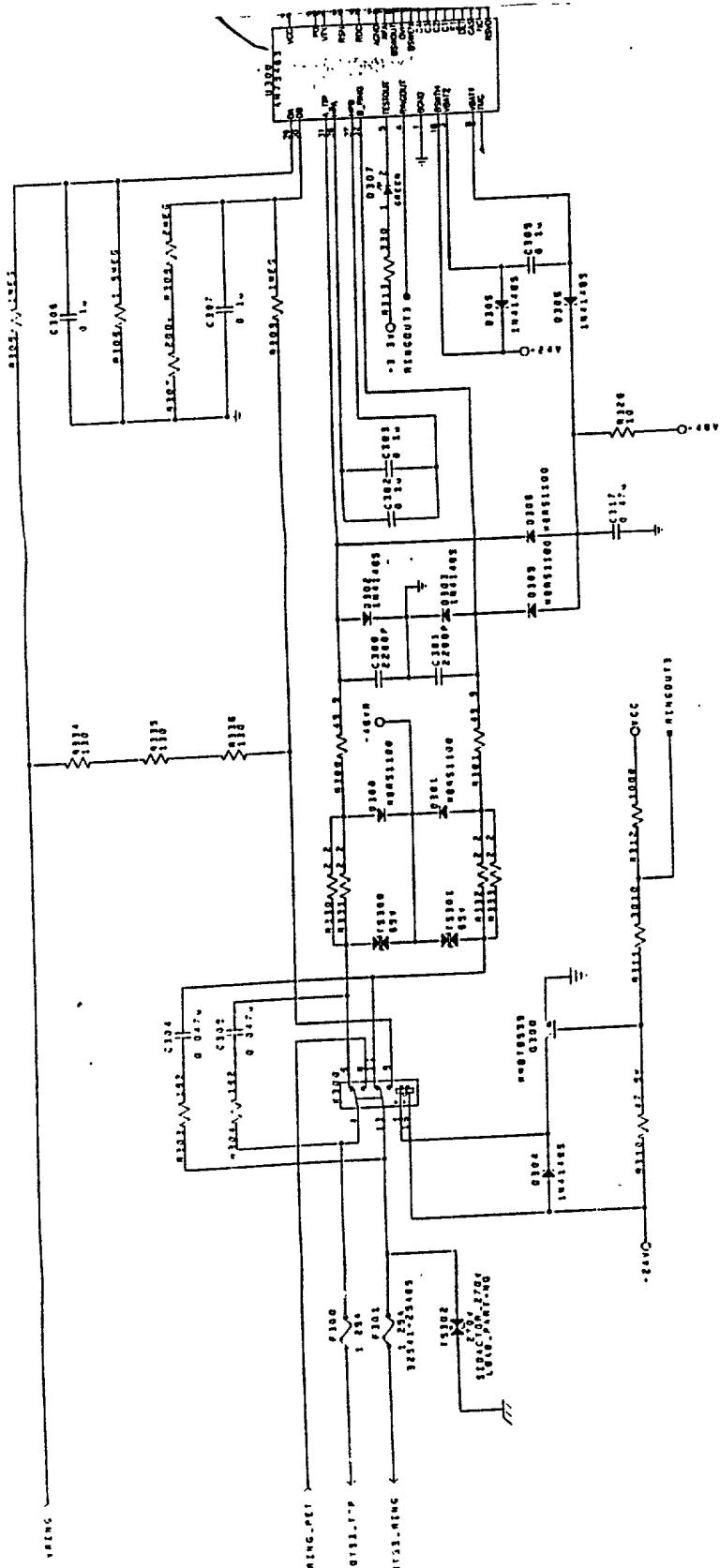
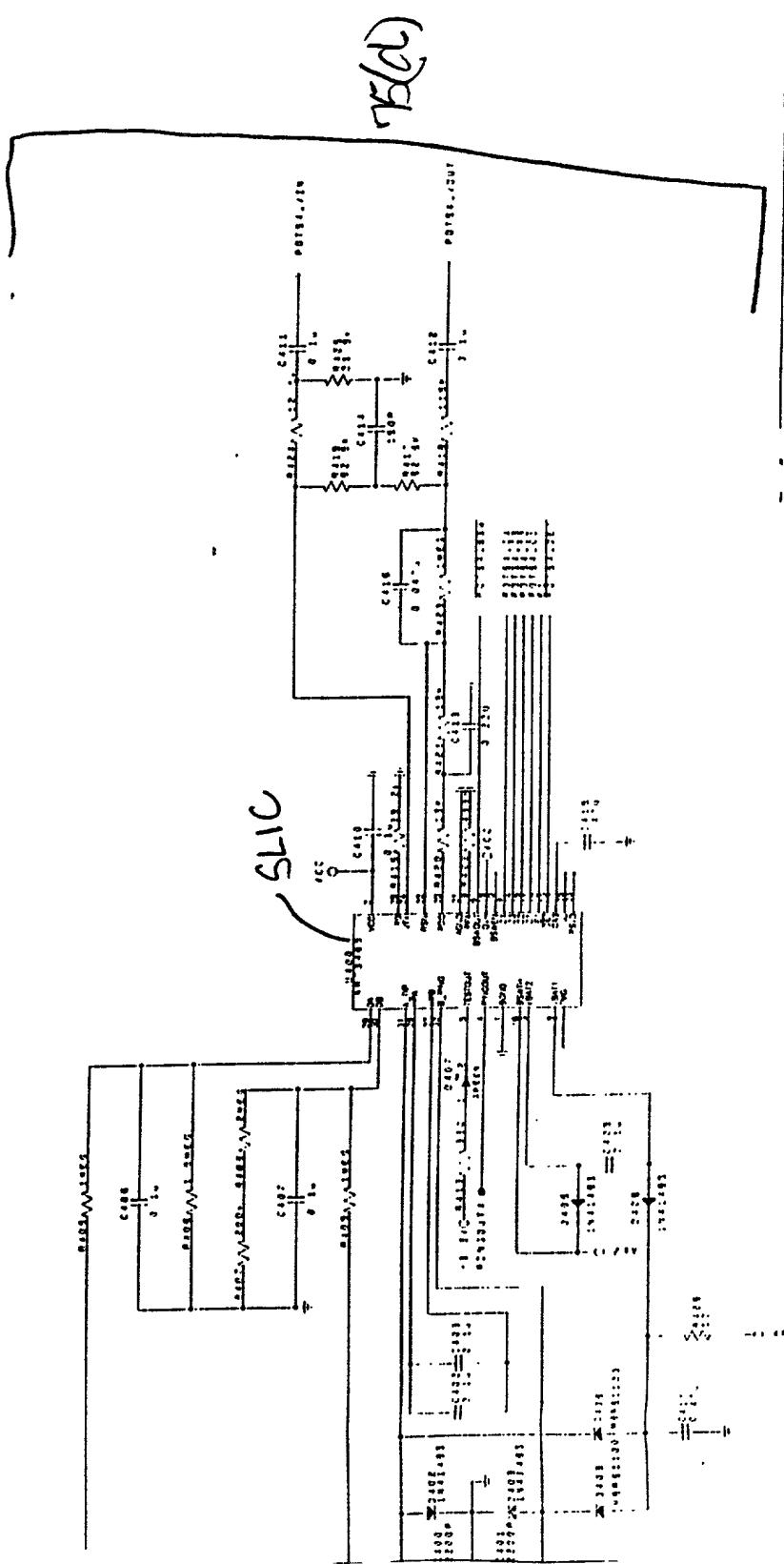


Figure 12(B) (Continued)

Fig. 12(B) (Continued)

Figure 12(B) (Continued)

TA 750 Quad POTS RT  
Schematic Sheet 1  
Drawing No. 2175408-1



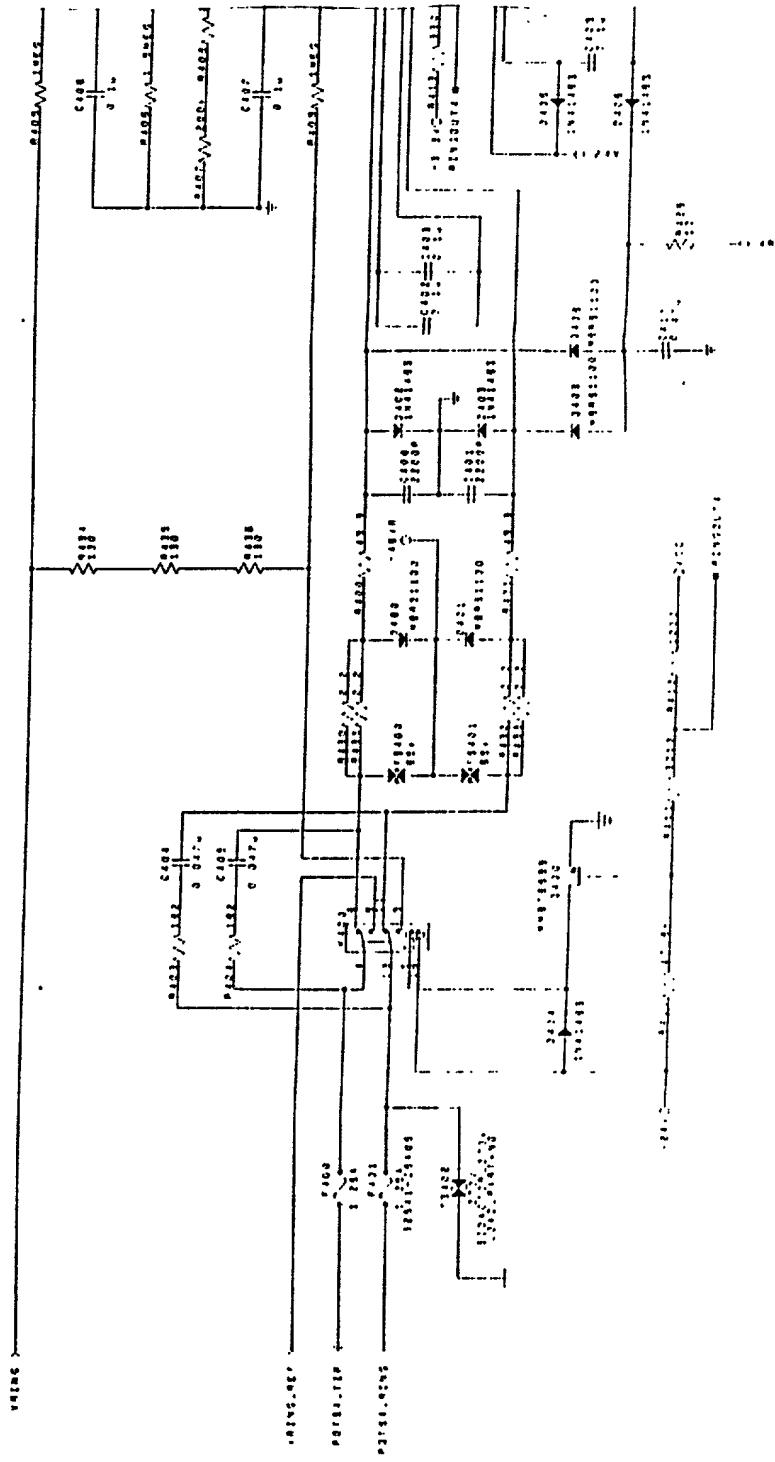
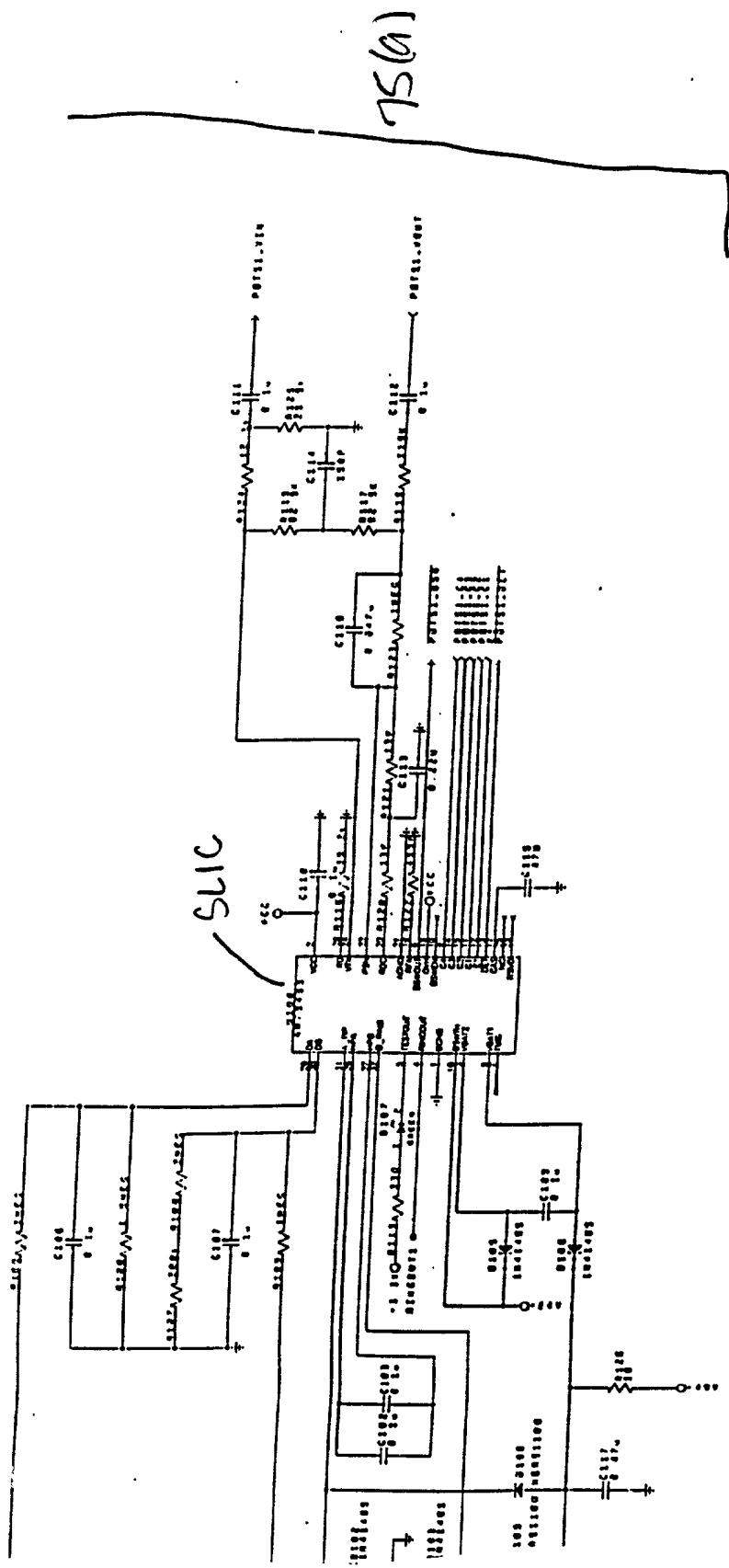


Figure 12(B) (Continued)

Fig. 12(b)

Figure 12(C)



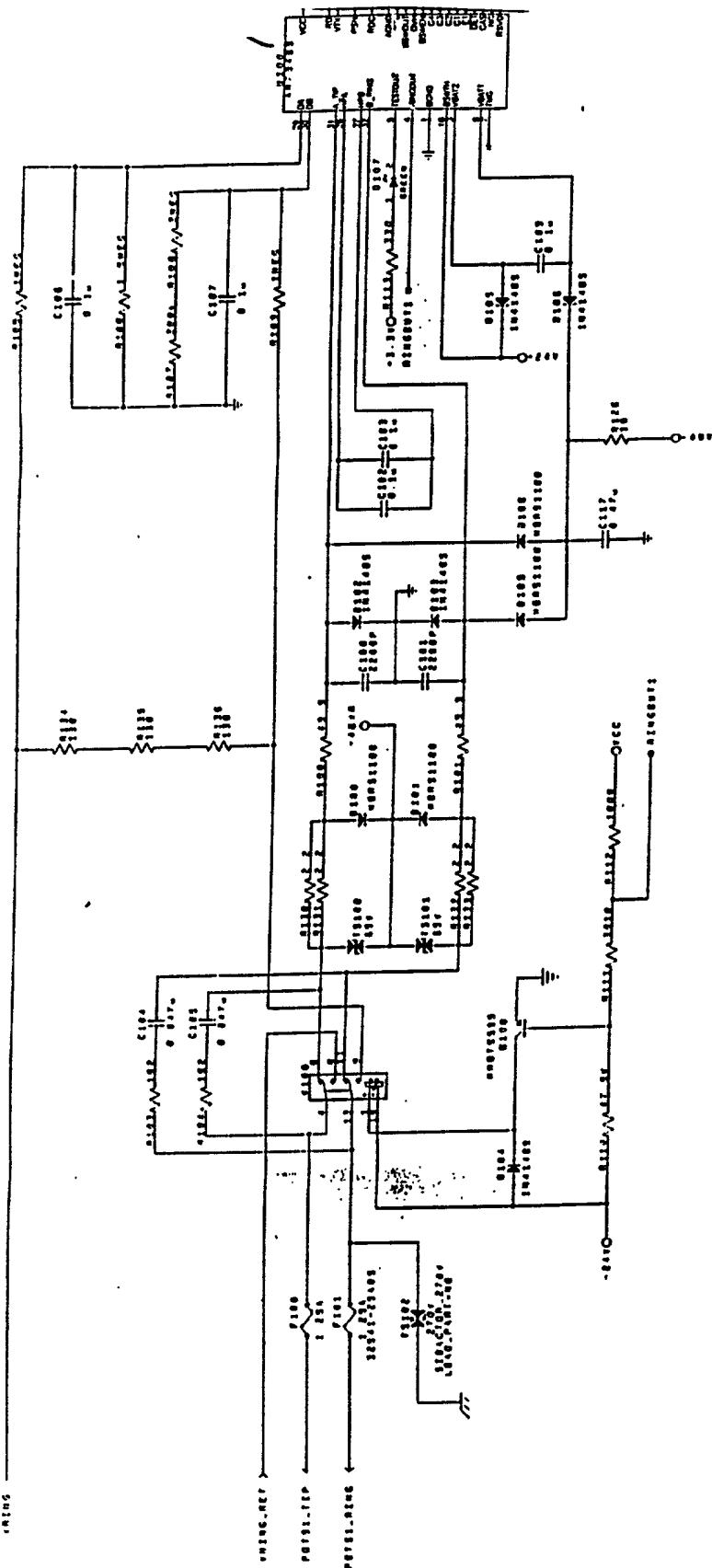


Figure 12(C) (Continued)

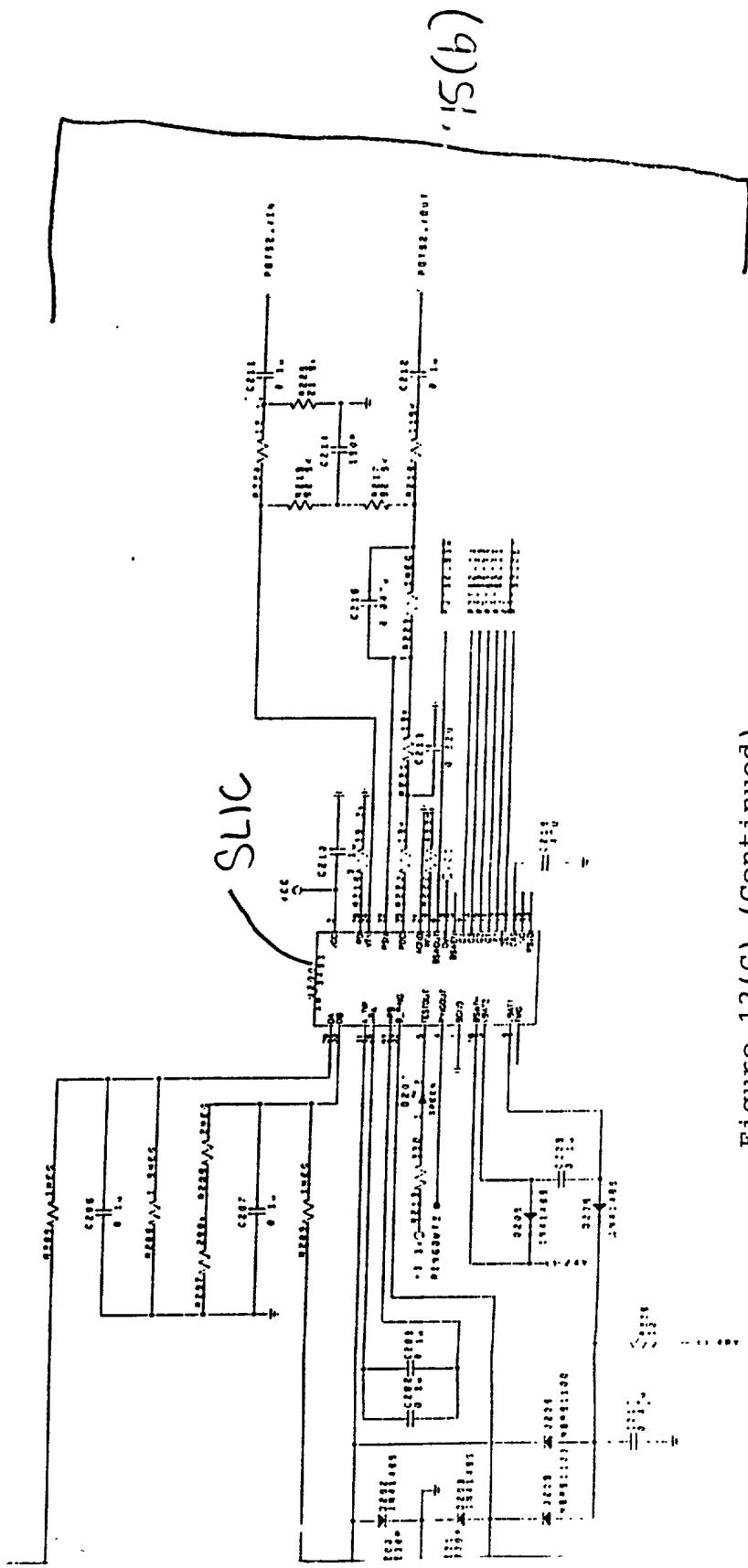


Figure 12(c) (Continued)

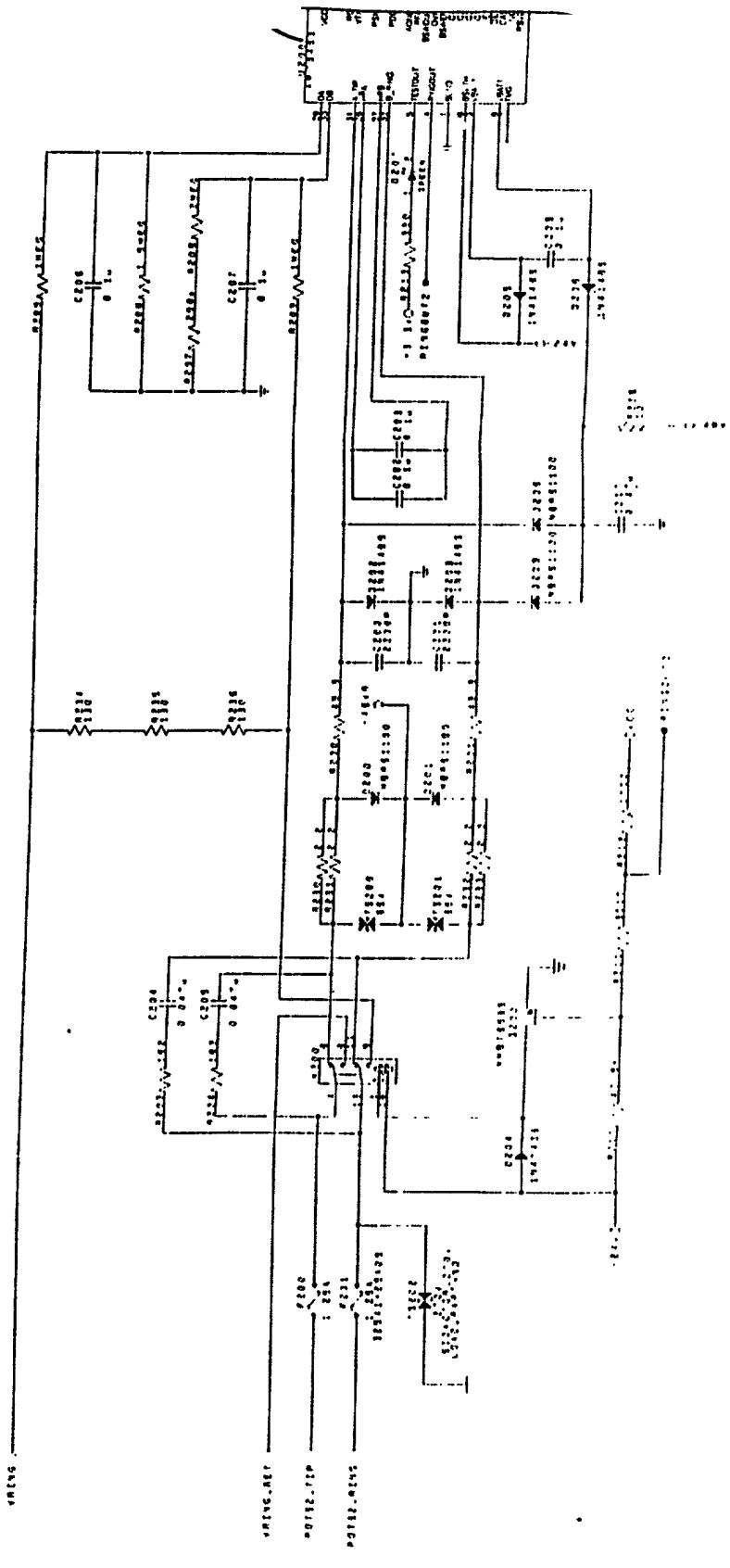


Figure 12(c) (Continued)

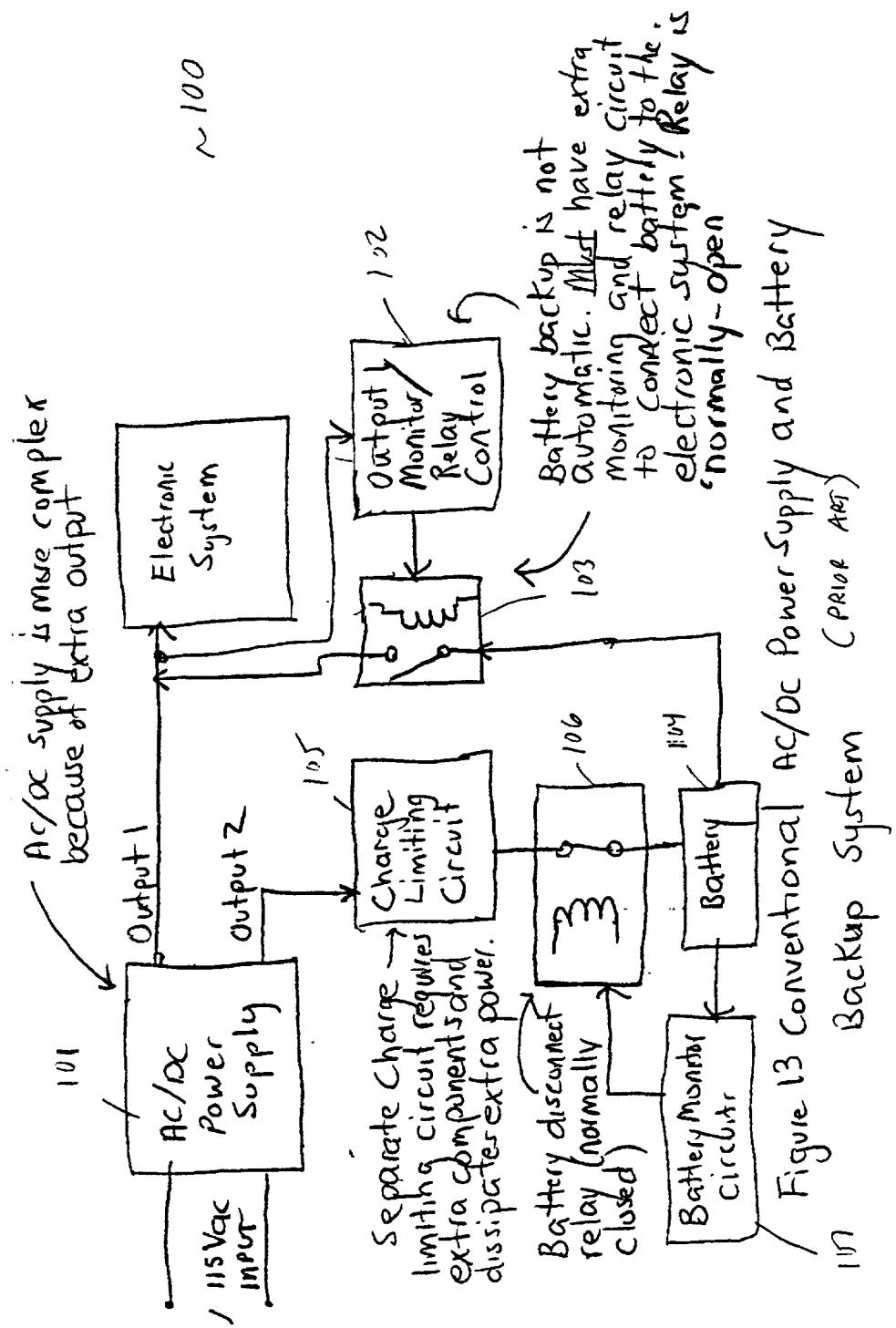


Figure 13 Conventional AC/DC Power Supply and Battery Backup System (prior art)

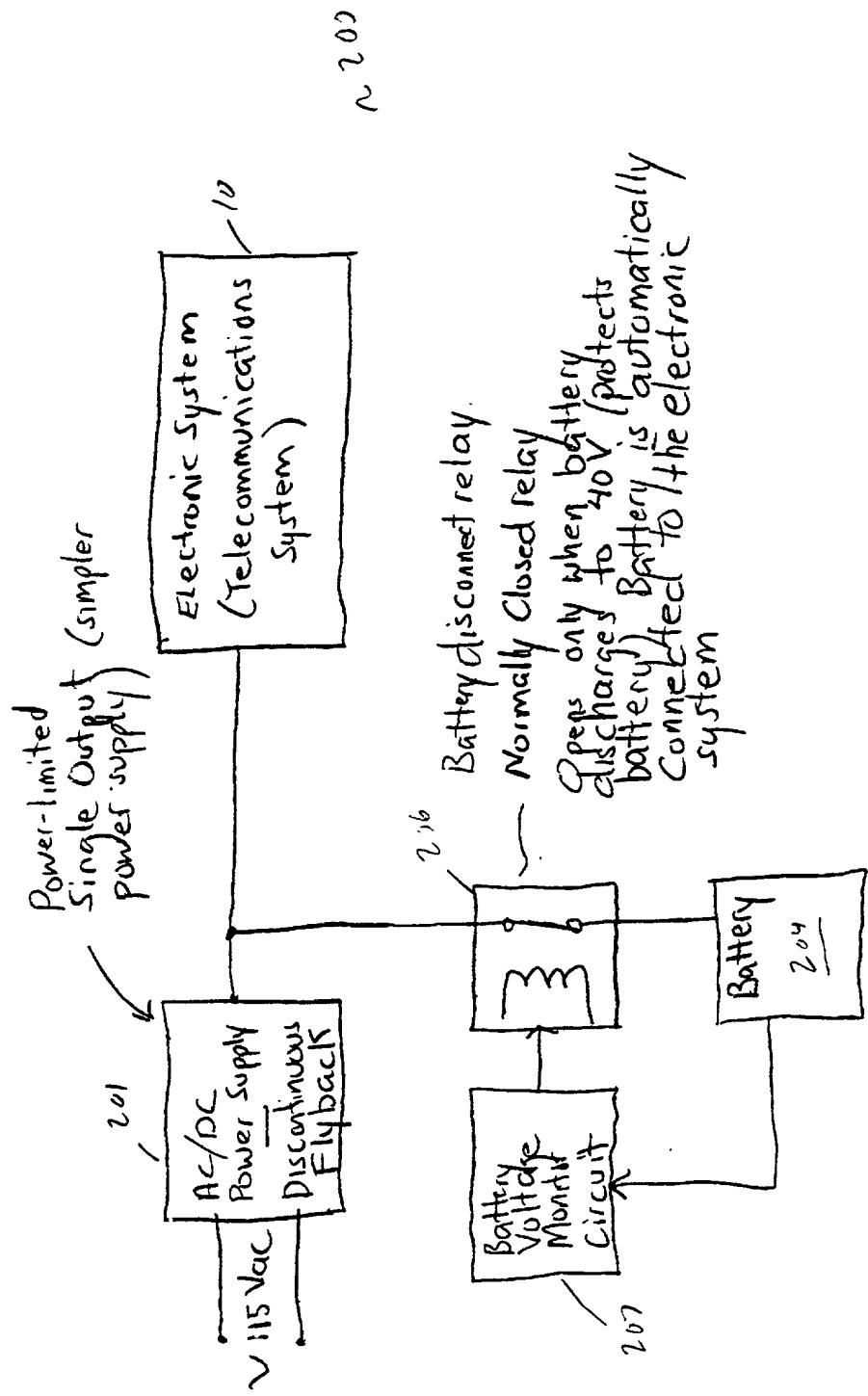


Figure 14 Block Diagram of New Power Supply/Battery Backup System

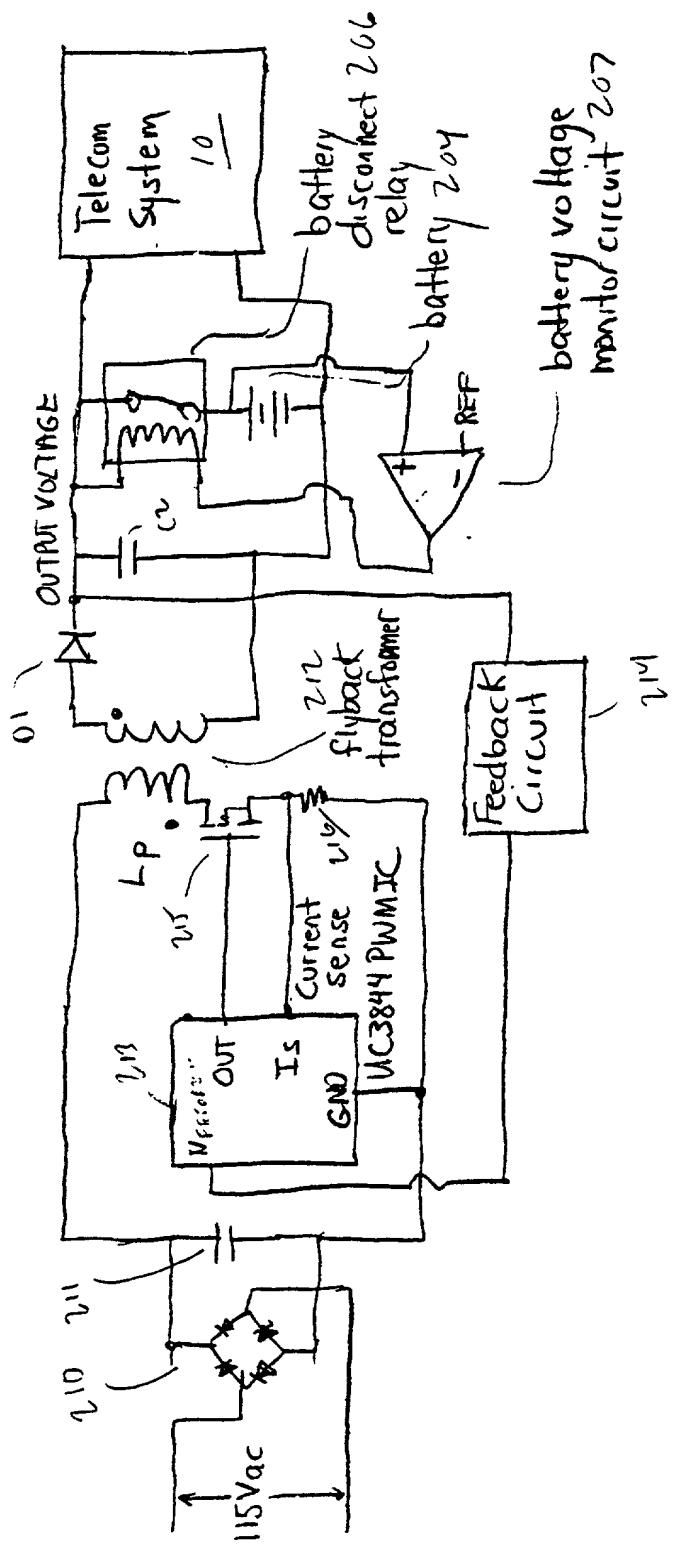


Figure 15 Simplified Schematic of New Power Supply/  
Battery Backup System ~ 200

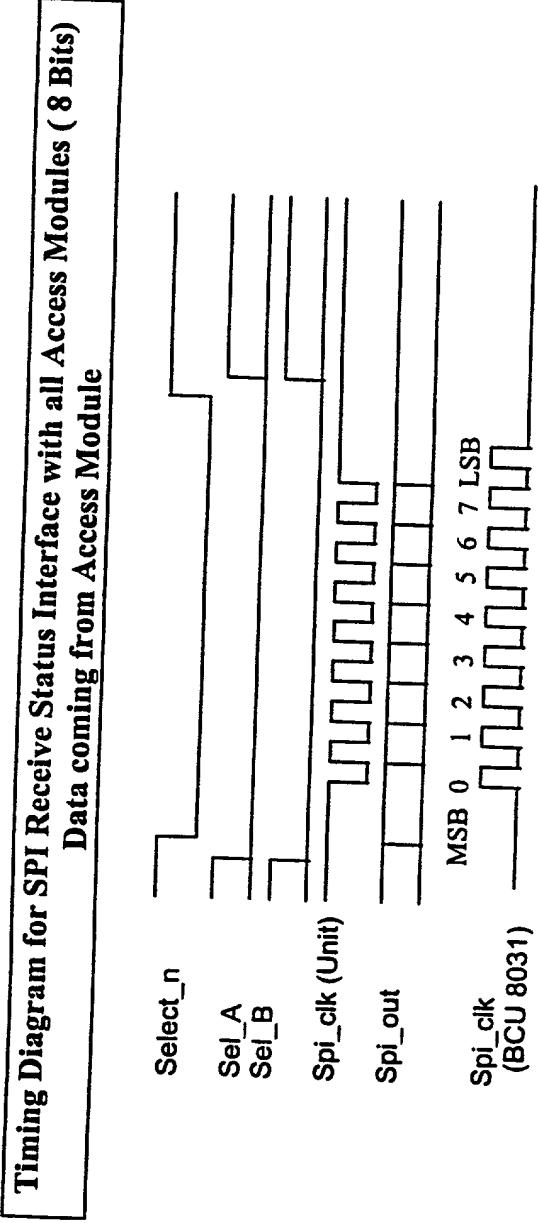


Figure 16

**Timing Diagram for SPI Receive Interface from Smart Access Modules for Provisioning (9 Bits) Data going into Access Module**

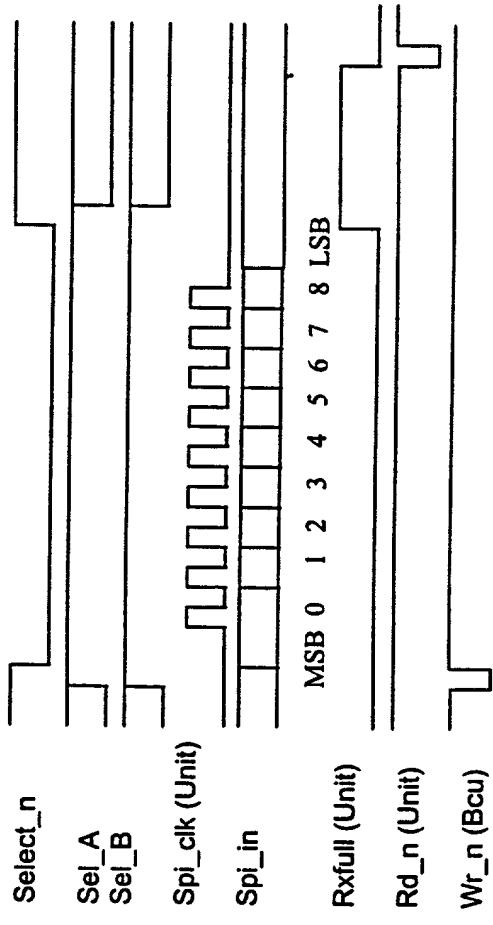


Figure 17

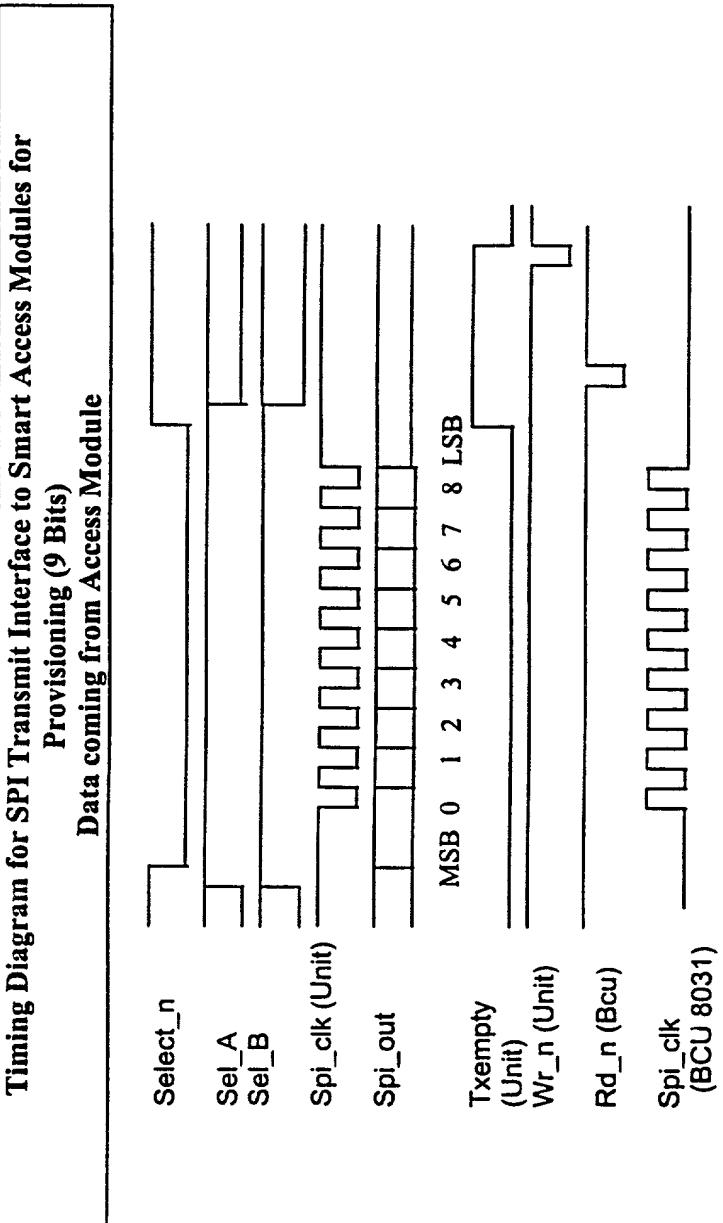


Figure 18

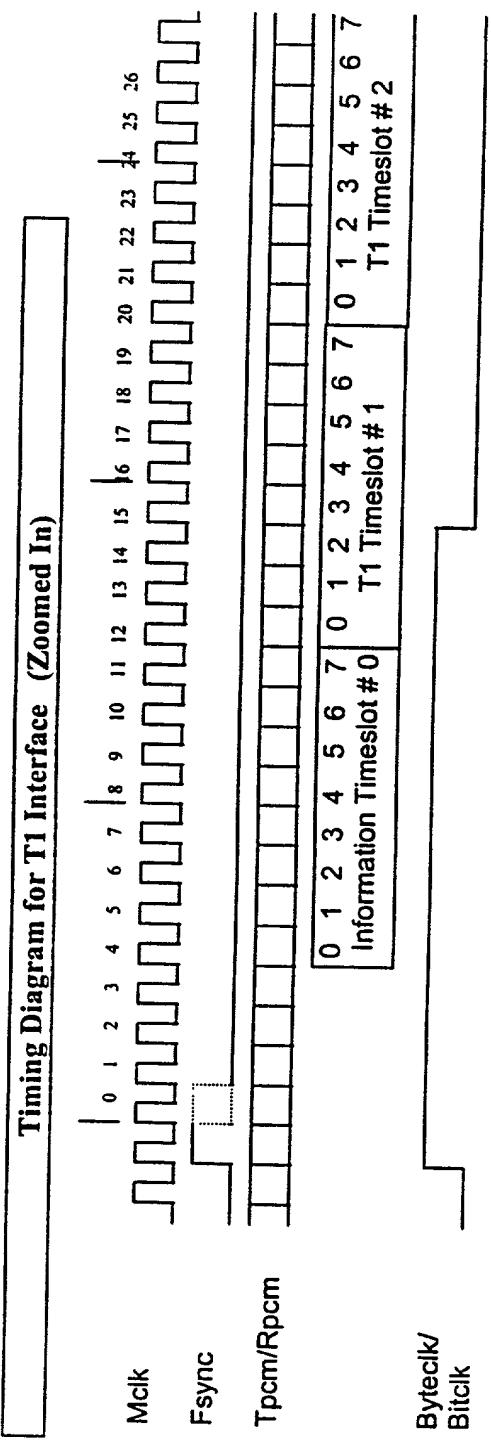


Figure 19

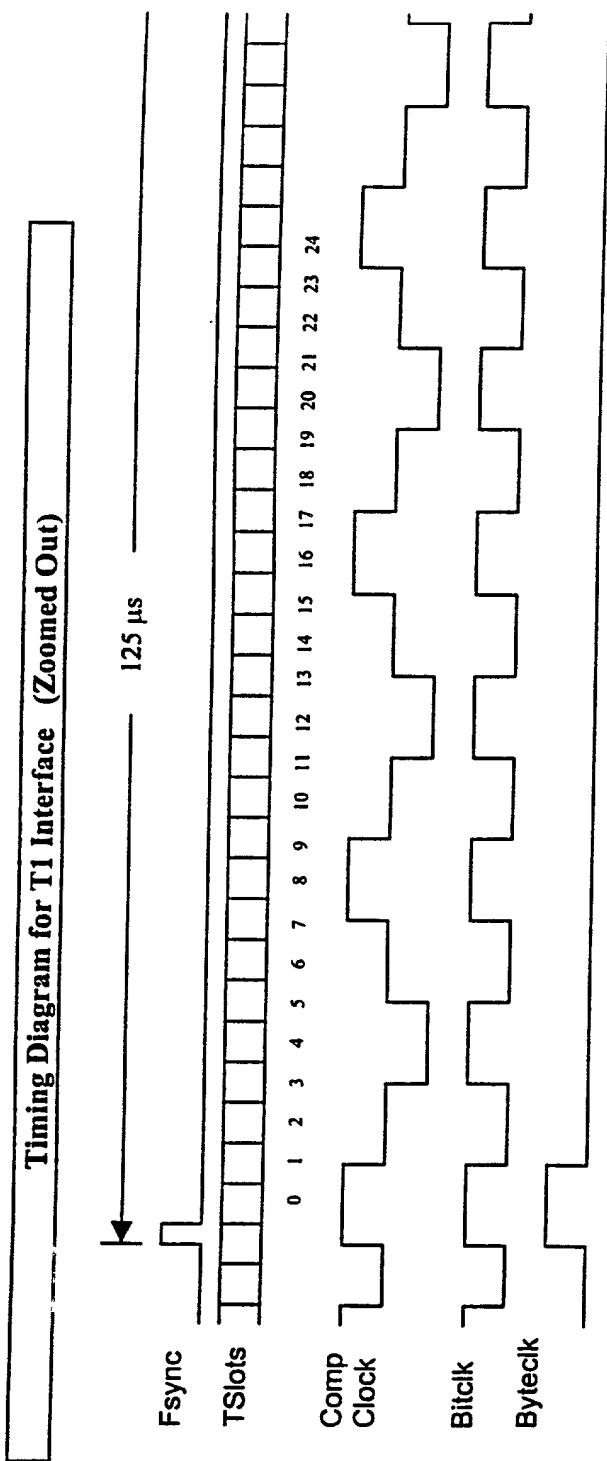


Figure 20